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RESEARCH ARTICLE

Photogeneration and Performance Optimization (PhPO): A New Algorithm to Improve the Performance of Vertical Epitaxial Hetero-Structure Architecture Laser Power Converters

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ABSTRACT The state of the art in the field of high-power laser transmission is dominated by the so called Vertical Epitaxial Hetero-Structure Architecture (VEHSA), which consists of monolithically stacking p/n cells connected by tunnel junctions. This configuration distributes the current between the cells and reduces the losses due to Joule heating. Since assessing the performance of each individual cell is very challenging, the design and optimization of these devices relies on simple approximations based on the Beer-Lambert law, the exponential decay of light, and guessing from published data. The limitations of these approaches are i) the current of each cell may differ from the calculated photocurrent, producing a mismatch and limiting the overall current, ii) the design parameters cannot be individually evaluated, and iii) the loss of accuracy when applied to devices with light trapping mechanisms. In this work, we present a novel optimization methodology aimed to overcome these limitations, based on a meticulous device Technology Computer-Aided Design (TCAD) and an iterative optimization algorithm with two stages: Photogeneration and Performance Optimization (PhPO). The proposed procedure improves the performance of the current state-of-the-art VEHSA devices, allows the use of new semiconductors and makes the design more resilient to a wide range of operation conditions.

INDEX TERMS Device modeling, new algorithm, laser power converters, VEHSA architecture, photo-current optimization, multijunction photovoltaic devices.

I. INTRODUCTION

The high power laser transmission (HPLT) technology has been pointed as a key development in the wireless power transfer (WPT) field [1]. It consists of transmitting power

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through a monochromatic light to a remote photovoltaic device, or laser power converter (LPC). This technology has the advantage of providing electrical isolation and avoiding electromagnetic interference [2], replacing traditional copper wires when security restrictions require the absence of sparks. Such is the case for workplaces under an ATEX directive, like refineries or mines where there is risk of fire or explosion [3]. Dual transmission of power and data [4], [5] and optically powering satellites [6], [7] or aerial vehicles [8], [9] are other possible applications of this technology. Another remarkable aspect is the possibility of deliver power through optic fiber or wireless transmission through free space [3].

Current state-of-the-art LPC devices exceed 60% efficiency values at room temperature [10], [11]. The base materials used are mostly III-V compounds, being GaAs the preferred one [12], since it achieves efficiencies much larger than LPCs based on Si [13] or InGaAsP/InP [14]. Multijunction GaAs LPCs are particularly noteworthy [15], as well as devices with enhanced photon recycling and treatment of thermalization losses [11]. New advances in photovoltaics provide hints for future efficiency improvements, such as new architectures [16], [17], [18], [19] or the use of materials with fewer limitations [20].

The Vertical Epitaxial Hetero-Structure Architecture (VEHSA) stands out among the multijunction LPCs. This arrangement consists of monolithically stacking p/n tunnel junctions [10], which is also the main pathway to achieve ultra-high efficiencies on concentrating photovoltaic solar cells [21]. The major advantage of this architecture is to distribute the current over the p/n junctions, which reduces Joule heating losses. This allows to operate under high laser power concentrations, increasing the efficiency due to the open circuit voltage (Voc) enhancement associated with higher carrier concentration. The Voc also benefits from a greater Fermi level splitting in thin layers [22]. However, the optimization of VEHSA devices is challenging because of the extreme difficulty of evaluating its individual cell currents. Up to date, the optimization is mainly done by simple approximations using the Beer-Lambert law [23], which only accounts for the photogeneration of each cell. This method is only meaningful to VEHSA devices made of well-known direct bandgap materials like GaAs, since design parameters like doping values and relative p/n sizes are extrapolable from published data [24] and the light decays exponentially in the bulk of the device, as no light-trapping mechanisms are needed. Nevertheless, not measuring the currents of each cell hinders the current matching and the optimization of design parameters, reducing the performance of the device. Although new optimizations paths have been described in literature, such as including tabulated quantum efficiency values in the photogenerated current estimation [25], [26], more sophisticated optimization techniques that broaden the range of VEHSAs are missing. This is of particular interest in the context of materials with indirect high-bandgap like silicon carbide, which has been identified as a potential new route to ultra-high efficiency LPCs [27].

In this paper we propose a new universal methodology to optimize the design of VEHSA devices. The procedure, based on combining device modeling with a multistage optimization algorithm, allows not only to further optimize stateof-the-art VEHSAs, but also opens the door to design and optimize new multijunction devices. This feature skips many trial and error processes when manufacturing novel VEHSA



FIGURE 1. Exponential decay of light intensity, according to Beer-Lambert law. To accurately match the photocurrents, the integrated photogeneration must be the same in all cells.

devices, considerably reducing costs. This methodology is valid for both direct or indirect bandgap materials, regardless of whether they are powered by laser or by sunlight.

II. METHODOLOGY

In this section we present the main limitations in the design of state-of-the-art VEHSA devices, and we introduce an optimization method to improve their performance via a meticulous current matching and the improvement of the individual efficiency of each cell.

A key factor that affects the performance of VEHSA devices is the precise matching of the currents produced by every cell, since the total current drawn from the device will be the lowest of all the contributing cells. Up to date, the state-of-the-art VEHSAs achieve current matching by applying the Beer-Lambert law, the exponential light decay [23]. The integrated photogeneration must be the same in all cells, as illustrated in **Figure 1**:

$$I_{i+1}(\lambda) = I_i e^{-\alpha(\lambda)t_i} \tag{1}$$

where I_i and I_{i+1} are the light intensities entering and leaving the i-th layer, respectively. t_i is the thickness of the i-th layer and $\alpha(\lambda)$ the wavelength dependent absorption coefficient. This is a good approximation for direct-gap VEHSA devices, as the photogeneration can be calculated very accurately. However, this method does not take into account the performance of each cell, so there will be



FIGURE 2. 2D-Schematic of a VEHSA device. The variable resistances in tunnel junctions allow to extract the current diferences from the adjacent cells. C1, C2, ..., Cn are the cells of the VEHSA device. The zoom-in shows the detailed structure of the cell C1, composed by n+/n/p/p+ layers, with a CH1 total height. PH1 and NH1 are the heights of C1 p and n layers, respectively.

differences between the photogenerated current and the total current contributed. To avoid this issue, it has been proposed to consider the quantum efficiency, QE, of each cell to calculate the current supplied [25] as follows:

$$I_{abs} = \int \frac{dI_{avail}}{d\lambda} \cdot QE(\lambda) \cdot (1 - T(\lambda) - R(\lambda)) d\lambda \quad (2)$$

where I_{abs} and I_{avail} are the absorbed and available photocurrents, *R* the reflectance, and *T* the transmittance. This has the advantage of considering beam reflection and transmission, and recombination effects, which are implicit in QE. However, it misses relevant effects such as photon recycling, which is shown to be important in the modeling of GaAs VEHSA devices [28].

These approaches generally provide good results, but the performance worsens as the number of cells increases, due to the current mismatch. The actual record efficiency for a VEHSA at room temperature is achieved by a 5-cell device, reaching a 66.3% [10], and the efficiency consistently decreases for a larger number of cells. Another disadvantage of these approximations is that they are not applicable to indirect bandgap devices that require back texturization for light trapping.

We present an optimization method aimed to overcome these issues valid for both direct or indirect bandgap VEHSAs, independently if they are powered by a laser or by solar light. The system consists of meticulous device TCAD (Technology Computer-Aided Design) of the VEHSA device combined with an iterative optimization algorithm. As modeling tunnel junctions in Silvaco can be problematic [29], we used an standard workaround that consist of modeling the tunnel junctions as perfect conductors [30], to speed-up the simulation time. In these so modeled tunnel junctions we allow the extraction of the tunnel junction currents (TJCs), coupling them to electrodes with lumped variable resistances, as seen in Figure 2. These resistances can take very high values to simulate the full device, to avoid current through these contacts, or low realistic values to obtain the differences between the currents produced in adjacent cells. This is a useful tool to accurately match the currents of all cells and improve the performance of the full VEHSA device.

The optimization algorithm is an iterative process involving two stages: Photogeneration and Performance Optimization (PhPO from now on), as shown in Figure 3. In the first step, the photogeneration loop, the total device height (DH) is optimized to absorb the largest part of the beam without unnecessarily increasing the size of the device, which will affect the carrier transport. As a first approach the Beer-Lambert law is used to obtain an initial guess. The algorithm iterates, by increasing/decreasing the DH, until two criteria are fulfilled: i) a minimum absorbed light value (photo_low), that establishes the maximum photogeneration losses allowed of the total incident light and ii) a maximum photogeneration value (photo_high), which prevents the device from growing infinitely to absorb the totality of the photons. The resulting optimum DH is then used as an input for the next stage, the performance loop.

In this second stage, the optimizer improves the performance of the cells individually. This process begins with a subroutine that iteratively evaluates the TJCs and modifies *CH1*,..., *CHn*, i.e. the relative sizes of each cell (see Fig' s 2 zoom-in). As the TJCs extracted are the differences between the currents of the adjacent cells, the absolute value represents their mismatch, and the sign of the current indicates which cell is limiting the performance. A negative/positive sign means that the bottom/top cell is limiting and the algorithm modifies the values of CH1, ..., CHn to increase the limiting cells in steps normalized by the largest absolute TJC value of all tunnel junctions. This process iterates until all TJCs contribute with the same current and the value is below a certain tolerance, e.g., 1/1000 of the Isc extracted in the anode. When this is achieved, the algorithm optimizes the design parameters, which are the layer relative sizes (PHi, NHi) and doping values (PDi, NPi) for every i-th cell, from 1 to n. The optimization algorithm for a single design parameter is described in Figure 4. The parameters are swept from an initial value (provided by a preliminary single cell optimization), one by one, with the scope of increasing the cell efficiency. If new optimum design parameters are found, a new iteration is needed, since these optimizations can change the current contributed by each cell and the currents need to be matched again. On the other hand, if during the whole cell optimization cycle no new design parameters are found, the process stops, and it is considered that all cells have achieved their maximum efficiency.

III. RESULTS

To validate our methodology we initially model a GaAsbased 5-cell VEHSA (VEHSA PT5 from now on), that is designed after the experimental device reported by Fafard et al [10], that currently holds the record breaking efficiency at room temperature. We carried the simulation with Silvaco Atlas [31], a device TCAD simulator able to provide realistic and trustable results when modeling a



FIGURE 3. Flowchart of the different stages of the PhPO method. Photogeneration TCAD and TJC TCAD are simulations aimed to obtain the photogeneration of each cell and the currents extracted in the tunnel junctions (TJC), respectively. The optimum device height is fixed in the photogeneration loop and used as an input to the performance loop.

wide variety of devices, including photovoltaic cells [17], [32]. The Poisson and continuity equations are solved to obtain the characteristics of the device. The beam-device



FIGURE 4. Detailed optimization of each design parameter. Efficiency TCAD is the simulation aimed to obtain the efficiency of the evaluated cell. η_0 , η , and η_{max} are the initial, latest simulated and maximum efficiencies of the evaluated cell, respectively.



FIGURE 5. Photogeneration rate in a VEHSA PT5.

interaction is modelled with the ray tracing method [31] and an example of the photogeneration rate for a VEHSA PT5 is pictured in **Figure 5**. For carrier mobility we use doping concentration-dependent tabulated data, available via Silvaco. The Shockley-Read-Hall (SRH) recombination also considers the doping concentration, following experimental

data [33]. We also include optical and Auger recombinations in the simulation framework. All simulations were performed at T = 298 K. The absorption coefficient used depends on both the doping concentration and the wavelength, and fits experimental curves [34]. The material physical parameters, the incident power and the illumination area are taken from the experimental device supporting information [10]. The width of the device (see Figure 2) is fixed to 10 μ m to save computational costs. Since the generation and transport processes occur in the vertical direction and no surface recombination is accounted in this work, which avoids perimeter recombination sources, this decision will not affect the behavior of the device. The contact finger (see Figure 5) covers a 3 percent of the width of the device. Given that no specific design parameters are provided for this particular experimental device, the cell heights (CH) were obtained by applying the Beer-Lambert law [23].

As the light penetrates the device, the CH values increase due to the photogeneration decay in the device, as seen in Figure 1. The CH for each cell includes the n+/n/p/p+layers as follows: CH = $h_{np+} C h_{np}$, where h_{np+} is the sum of the p+/n+ layers heights and h_{np} the sum of the n/p layer heights. The p+/n+ layers have fixed heights and doping values of 0.02 μ m and 5.10¹⁹ cm⁻³, respectively, selected to accurately match the experimental characteristics. The n/p relative layer heights (NH/PH) are shown as a percent of h_{np} (e.g. n layer height = NH· h_{np}), since this value is fixed by the CH and h_{np+} . The NH/PH values are considered to be equal (0.50/0.50) [10], and the doping values (ND/PD) range from $5 \cdot 10^{17} - 1.0 \cdot 10^{18}$ cm⁻³, with thinner cells more heavily doped [25]. The illumination wavelength is fixed to $\lambda = 837$ nm as in the experimental device. These initial parameters at room temperature are shown in **Table 1**. Figure 6 shows a comparison between the experimental and the initial calibrated VEHSA PT5 IV curves. Note that in this figure the simulation results are scaled to the illumination area of the experimental device. The device TCAD accurately reproduces the values of I_{sc} and V_{oc} of the experimental device, and the slight differences in the shape of the curve at V_m and intermediate voltages may derive from manufacturing issues.

Once we have demonstrated the validity of our simulation methodology, we apply the PhPO method to further improve the device performance, see in Table 1 the optimized design parameters. The DH slightly increases with respect to the calibration, reaching 3.7 μ m. The cell heights have been fine-tuned until the TJC mismatch is below 1/1000 the I_{sc} value of the anode. The relative NH/PH values return much larger n layer heights for thinner cells (e.g., 0.9/0.1 for the first cell), that gradually decrease as the CH increases (e.g., 0.3/0.7 for the fifth cell). The doping values significantly decrease to 1 \cdot 10¹⁵ cm⁻³, a value three orders of magnitude lower than those observed in the initial design parameters.

The optimized IV curve is also shown in Figure 6 for comparison. The optimized VEHSA PT5 achieves the same V_{oc} as the calibrated and experimental devices and increases



DP	DH [µm]	Cell	CH [µm]	NH [%] / PH [%]	ND [cm ⁻³]	PD [cm ⁻³]
Initial	3.5	1 2 3 4 5	0.170 0.218 0.303 0.503 2.306	0.50/0.50 0.50/0.50 0.50/0.50 0.50/0.50 0.50/0.50	1e18 1e18 1e18 1e18 5e17	1e18 1e18 1e18 1e18 5e17
Optimized	3.7	1 2 3 4 5	0.175 0.227 0.317 0.530 2.450	0.90/0.10 0.90/0.10 0.60/0.40 0.50/0.50 0.30/0.70	1e15 1e15 1e15 1e15 1e15	1e15 1e15 1e15 1e15 1e15 1e15
Constant	T [K] 298	Width [µm] 10	N ^{+/} P ⁺ H [μm] 0.02	N ^{+/} P ⁺ D [cm ⁻³] 5e19	λ [nm] 837	CF [%] 3



FIGURE 6. IV curves comparing the experimental [12] VEHSA PT5 against the optimized structure provided by the PhPO method. The calibrated VEHSA PT5 is also included as validation.

the I_{sc} by $\approx 6\%$, leading to a 75.8% efficiency, a value 9.5% larger than that of the experimental VEHSA PT5. These improvements are due to the meticulous current matching and individual cell optimization. This can be more clearly seen in **Figure 7** and **Figure 8**, that present the IV curves for each individual cell for the calibrated and optimized devices, respectively. Note that the calibrated VEHSA PT5 individual cell IV curves suffer from current mismatch, and there is also a noticeable V_{oc} mismatch. These effects are drastically reduced in the optimized VEHSA PT5, where the IV curves are very similar for all cells.

Given the wide variety of applications for an LPC, both aerial and terrestrial, it is worthwhile to study the impact of temperature on device performance. For that, the calibrated and optimized VEHSA PT5, are tested at three temperatures:



FIGURE 7. IV curves for each cell that composes the calibrated VEHSA PT5. Note that there is an appreciable dispersion in the values of I_{sc} and, to a lesser degree, in those of V_{oc} .



FIGURE 8. IV curves for each cell that composes the optimized VEHSA PT5. Note that, unlike in the case of the calibrated device, there is no dispersion observed in either I_{sc} or V_{oc}.

the standard laboratory conditions (298 K) and \pm 75 K with respect to this value (223 K and 373 K), in order to evaluate a wide range of operation. The models [35] used in the course of the simulations take into account the dependence on temperature, namely the universal energy bandgap model, SRH concentration-dependent lifetime model, Fermi statistics and power law temperature dependence mobility.

Figure 9 shows the IV curves for the optimized and calibrated VEHSAs PT5 at the three tested temperatures. Note that, although both devices reach the same V_{oc} for each temperature (due to bandgap variation), the optimized VEHSA PT5 achieves the same I_{sc} for all three temperatures tested. This is not the case for the calibrated VEHSA PT5, which suffers from loses in I_{sc} at high temperatures. This is due to the existence of higher recombinations in the experimental device, compared to the optimized one. In the



FIGURE 9. IV curves for the optimized (solid lines) and calibrated (dotted lines) VEHSA PT5 at three temperatures: 223 K (blue), 298 K (green) and 373 K (red).

calibrated VEHSA PT5, the efficiency increases/decreases a 11.3% at 223/373 K with respect to the room temperature values. The efficiency of the optimized VEHSA PT5 is slightly less affected by temperature variations, increasing/decreasing a 10.3% at 223/373 K with respect to the room temperature values, respectively. The design parameters obtained through the PhPO method applied at 298 K have considerably reduced the recombinations of the VEHSA PT5, making the device more resilient to a wide range of temperature. Note that, having a single device capable of operating over a broad temperature spectrum without major losses is desirable to avoid the need to manufacture multiple devices.

IV. CONCLUSION

We have presented a new method to optimize the design parameters of VEHSA devices, one of the most efficient architectures of state-of-the-art LPCs. The Photogeneration and Performance Optimization (PhPO) method combines device TCAD with an iterative optimization algorithm. This method is aimed to bypass the main limitations of the VEHSA architecture, namely the current mismatch between cells and the impossibility of tailoring individual cell design parameters. Traditionally, the Beer-Lambert law of exponential decay is the main available tool to match the different cells photogeneration. However, this method obtains low accuracy results when a back reflector or light-trapping mechanisms are included. Another issue related to only applying Beer-Lambert law is that the individual currents of each cell may differ, due to the different performance of the cells, leading to current mismatch.

To validate the PhPO method, we initially modelled the current room temperature record efficiency VEHSA device, a GaAs-based 5-cell reported by Fafard et al. We have accurately reproduced the experimental device IV curves at the same illumination conditions, using similar design parameters. Next, we have applied the iterative optimization algorithm to the calibrated VEHSA. The algorithm has successfully matched all cells I_{sc} and V_{oc} values and improved each cell performance. This has led to a 9.5% efficiency increase of the optimized device with respect to the experimental VEHSA.

Given the diverse applications of LPCs, we have tested both the optimized and calibrated VEHSAs at 3 different temperatures: 223 K, 298 K and 373 K. The optimized device is slightly more resilient to temperature changes due to a reduction of the recombination effects achieved by the optimization of design parameters.

In conclusion, the PhPO method provides optimum design parameters for state-of-the-art and new generation VEHSA devices, opening a new route towards ultra-efficient VEHSAs with application to LPC and tandem solar cells, not necessarily based on direct bandgap materials.

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